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(54) INPUT/OUTPUT CIRCUIT

(57) Abstract:

PROBLEM TO BE SOLVED: To prevent all transistors(TRs) inside an LSI from being destroyed.

SOLUTION: This input/output circuit consists of a bonding pad 2 that is connected to a high voltage signal line at the outside of an LSI internal power supply voltage, high level output circuit sections 29, 31 consisting of a floating n-well system P-channel TRs and an analog switch and outputting an LSI internal signal to an external device, low level output circuit sections 30, 32 consisting of series connection of a plurality of N channel TRs and outputting the LSI internal signal to an external device, and an internal control circuit section 48 that provides a difference to a conduction time of the high level output circuit sections 29, 31 and the low level output circuit sections 30, 32. The

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internal control circuit section 48
generates a control signal to control
the high level output circuit sections
29, 31 and the low level output circuit
sections 30, 32.

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